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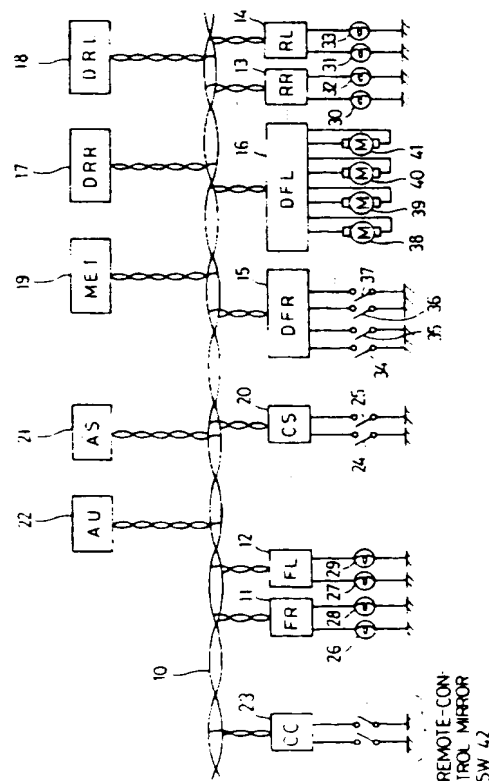
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(54) **Multiplex transmission system.**

(57) A multiplex transmission system of this invention includes a plurality of multiplex nodes (11-23) interconnected by a common multiplex transmission line (10). A plurality of groups of different combinations of the multiplex nodes (11-14, 19, 20) are predetermined, and a frame of data is transmitted among the multiplex nodes (11-23) through the multiplex transmission line (10). The data frame includes a group specifying area specifying a group of nodes (11-14, 19, 20) from which an acknowledge signal is to be returned. When a data frame transmitted from a sending node (20) is received, each multiplex node (11-23) determines whether or not it belongs to the group (11-14, 19, 20) specified by bits binary code in the group specifying area of the transmitted frame. When the data frame is properly received, only those multiplex nodes (11-14, 19, 20) which belong to the specified group return the acknowledge signals to an acknowledge signal area in the data frame in accordance with the bits in the group specifying area, whereby the transmission efficiency is improved.

FIG. 1



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This invention relates to a multiplex transmission system for carrying out a data transmission among a plurality of multiplex nodes connected to a common multiplex transmission line, and more particularly, to a multiplex transmission system using CSMA/CD (carrier sense multiple access/collision detection).

In conventional multiplex transmission systems of this type, a plurality of terminals (multiplex nodes) are connected to a common multiplex transmission line (multiplex bus) formed of, e.g., a paired cable. One of the multiplex nodes sends a predetermined data frame to the multiplex bus in accordance with the CSMA/CD method, to thereby transmit data simultaneously to the other multiplex nodes. The data frame includes an acknowledge signal (ACK signal) area at an end thereof.

In such systems, it is known that each of the multiplex nodes that received the data frame returns an ACK signal to a bit position of the ACK signal area preassigned thereto.

The multiplex nodes each include a computer for communication, and these computers operate independently of one another and are able to send a data frame to the multiplex bus at desired timing. Therefore, a collision of transmitted messages can occur on the multiplex bus, and conventionally this is prevented by setting a priority sequence for the transmission of individual messages.

In accordance with the priority sequence thus set, each computer carries out a priority control to send a message without destroying same. Specifically, each computer carries out a data transmission in accordance with a nondestructive arbitration type CSMA/CD access method in which the transmission of a message with a lower priority is automatically interrupted and only the higher-priority message is continuously transmitted. When the data is properly received, each multiplex node returns an ACK signal to a location of address uniquely assigned thereto (respective bit area in the ACK signal area).

If a local error occurs in the network and the data received by a multiplex node is erroneous, then no ACK signal is returned from this multiplex node, and therefore, the sending multiplex node determines that an abnormal situation occurred, and retransmits the data. The sending multiplex node repeats the transmission of the data, e.g., three times at the maximum, until all of the registered multiplex nodes return the ACK signals. In prior art systems, all nodes have the function of determining whether or not ACK signals have been returned from the nodes and retransmitting data.

A sending multiplex node uses an ACK management function, i.e., if a node fails to return the ACK signal even though the data transmission has been repeated three times, such a node is regarded as being in trouble and is excluded from the registered nodes, and when an extra ACK signal is received, the

node corresponding to this ACK signal is registered.

In this multiplex transmission system, since all of the nodes connected to the network return ACK signals, the ACK signal area occupies a large portion of the frame if the number of nodes connected to the network is increased, and thus a problem arises in that the transmission efficiency is lowered during a frame transmission. In other words, to ensure a certain transmission efficiency, the number of nodes connectable to the network is limited.

This invention was contrived in view of the above circumstances, and an object thereof is to provide a multiplex transmission system capable of carrying out a data frame transmission without lowering the transmission efficiency.

Another object of this invention is to provide a multiplex transmission system permitting a significant increase in the number of nodes connectable to a network.

The above objects can be achieved by a multiplex transmission system of this invention which has a plurality of multiplex nodes interconnected by a common multiplex transmission line for transmitting a frame of data among the multiplex nodes through the multiplex transmission line, wherein, when a data transmission is properly carried out, an acknowledge signal indicating a proper completion of the data transmission is returned to an acknowledge signal area provided in the frame of data corresponding to the respective multiplex nodes, the frame of data including an identifier which specifies one of groups of predetermined combinations of multiplex nodes from which the acknowledge signal is to be returned, and the multiplex nodes which belong to a specified group returning the acknowledge signal in accordance with the identifier in the transmitted frame of data.

In the multiplex transmission system according to this invention, the multiplex nodes connected to the common multiplex transmission line detect the identifier in the transmitted data frame. Among these multiplex nodes, only those multiplex nodes which belong to the group specified by the identifier return the acknowledge signals, whereby the transmission efficiency is improved.

The above and other objects, features, and advantages of this invention will become more apparent from the ensuing detailed description, which is by way of example only, taken in conjunction with the accompanying drawings, in which:

FIG. 1 is a diagram showing the arrangement of an automobile multiplex transmission system using a nondestructive arbitration type CSMA/CD method, according to this invention;

FIG. 2 is a diagram illustrating a message format for data frames according to a first embodiment of this invention;

FIG. 3 is a diagram showing the structure of a PWM signal used for a transmission code in the

multiplex transmission system;

FIG. 4 is a schematic diagram showing the arrangement of a multiplex node used in this invention;

FIG. 5 is a chart showing a data frame at different stages, for illustrating a transmission procedure of the multiplex transmission system according to this invention;

FIG. 6 is a diagram illustrating an updating of an ACK table according to this invention; and

FIG. 7 is a diagram illustrating a message format for data frames according to a second embodiment of this invention.

Referring to FIG. 1, a multiplex transmission system is installed in an automotive vehicle using a non-destructive arbitration type CSMA/CD access system. This multiplex transmission system comprises a plurality of multiplex nodes which include, e.g., a front-right (FR) multiplex node 11, a front-left (FL) multiplex node 12, a rear-right (RR) multiplex node 13, a rear-left (RL) multiplex node 14, a door front-right (DFR) multiplex node 15, a door front-left (DFL) multiplex node 16, a door rear-right (DRR) multiplex node 17, a door rear-left (DRL) multiplex node 18, a meter (MET) multiplex node 19, a combination switch (CS) multiplex node 20, an air conditioner switch (AS) multiplex node 21, an air conditioner unit (AU) multiplex node 22, and a center console (CC) multiplex node 23; and a multiplex bus 10 formed of, e.g., a shielded twisted pair cable. These multiplex nodes 11 through 23 are connected in a multi-drop fashion to the multiplex bus 10 to constitute a network. In this network, signals containing vehicle drive information are transmitted serially in units of data frames in a multiplex mode.

Among these multiplex nodes, the FR multiplex node 11 and the FL multiplex node 12 are supplied, respectively, with a signal from a headlight switch 24 and a signal from a turn signal switch 25, through the CS multiplex node 20. In accordance with these signals, the FR and FL multiplex nodes 11 and 12 turn on and off right and left headlights 26 and 27 and right and left turn signal lights 28 and 29 connected thereto.

Signals from the headlight switch 24 and turn signal switch 25 are also supplied to the RR and RL multiplex nodes 13 and 14, and in accordance with these signals, the multiplex nodes 13 and 14 turn on and off right and left rear tail-lamps 30 and 31 and right and left turn signal lights 32 and 33.

The DFR multiplex node 15 is supplied with signals from a door lock switch 34 and door unlock switch 35, as well as signals from power window up switches 36 and power window down switches 37 associated with individual doors. These signals are transmitted in the form of multiple signals to the DFL multiplex node 16 and the other door multiplex nodes 17 and 18. For example, when a door lock signal or door unlock signal is received from the door lock switch 34 or door un-

lock switch 35, the DFL multiplex node 16 drives a door lock motor 38 or door unlock motor 39 in accordance with the received signal. When a power window up signal or power window down signal is received from the power window up switch 36 or power window down switch 37, the DFL multiplex node 16 drives a power window up motor 40 or a power window down motor 41 in accordance with the received signal.

The CC multiplex node 23 is supplied with signals from remote-control mirror switches 42, and these signals are then transmitted in the form of multiple signals to the DFR and DFL multiplex nodes 15 and 16. Upon receiving these signals, the DFR and DFL multiplex nodes 15 and 16 drive respective motors to adjust the mirror positions in accordance with the signals.

A large number of signals are transferred between the AU multiplex node 22 which controls the air conditions in the automobile and the AS multiplex node 21 which controls the switches of an air conditioner. Various indicators, not shown, are connected to the MET multiplex node 19.

In this type of automobile multiplex transmission system, multiplex nodes that are closely related can be considered to form a group. In the illustrated embodiment, for example, the FR, FL, RR, RL, MET and CS multiplex nodes 11 to 14, 19 and 20 can be regarded as a group related to the lighting, the DFR, DFL, DRR, DRL and CC multiplex nodes 15 to 18 and 23 can be regarded as a group related to the doors, and the AS and AU multiplex nodes 21 and 22 can be regarded as a group related to the air conditioner. In the case of grouping the nodes in this manner, it can be said that, while a large number of signals are transferred within the individual groups, a relatively small number of signals are transferred between different groups. In this multiplex transmission system, some signals are, though not large in number, transmitted without regard to the grouping. For example, a signal from the ignition switch contains information required by all nodes for carrying out a power supply control.

The multiplex transmission system of this invention is particularly advantageous in the case in which nodes can be classified into groups, as mentioned above. In this embodiment, the multiplex nodes are grouped according to functions (each group including eight nodes at the most). Namely, in the embodiment, nodes 0 to 7 constitute a first group, nodes 8 to 15 constitute a second group, nodes 16 to 23 constitute a third group, and nodes 24 to 31 constitute a fourth group.

FIG. 2(a)-(d) illustrate a message format for data frames according to one embodiment of this invention. The frames shown in FIG. 2 each comprise an SOM (start-of-message) indicating the start of the message, a message ID indicating the contents of each of subsequent data (DATA) (the message ID may at the same time include a priority used for de-

termining a priority sequence when a plurality of multiplex nodes simultaneously send data), a group specifying area, described later, data areas (DATA 1 to DATA 3) having a fixed or variable length, an error check code such as a CRC (cyclic redundancy check code), an EOD (end-of-data) indicating the end of the data, an ACK signal area to which ACK signals are returned from all of the multiplex nodes specified in the group specifying area on a bit-wise basis, and an EOM (end-of-message) indicating the end of the message.

The feature of the message format resides in that the transmitted frame includes the group specifying area specifying a group (first, second, third or fourth group) of multiplex nodes from which ACK signals are to be returned in response to the frame. In the illustrated embodiment, the multiplex nodes belonging to the group specified by the group specifying area return ACK signals following the end-of-data in the ascending order of addresses. Namely, the first to fourth groups are specified in the group specifying area of a transmission frame from the least significant bit thereof, i.e., "0001" to "0100" respectively. When a certain group is specified in the group specifying area, the nodes belonging to the specified group successively return ACK signals following the end-of-data. In this embodiment, a plurality of groups can be simultaneously specified, as shown in FIG. 2(d). For example, when the first and fourth groups are to be simultaneously specified, "1001" is set in the group specifying area of the frame.

The ACK signal area may be constituted by various transmission codes. An example is shown in FIG. 3, in which a PWM (pulse-width modulated) signal is used, to facilitate the return and reception of the ACK signal by each multiplex node.

In the illustrated PWM signal, one logical bit is divided into three bit serpents (hereinafter referred to as "phases"). First and third phases of the PWM signal have fixed levels, i.e., they have active and passive levels, respectively, and a second phase may have an active or passive level. Namely, when the second phase of the PWM signal is active, then it means logical "1," and when the second phase is passive, it means logical "0."

When data is properly received, each of the multiplex nodes of the specified group returns an ACK signal to a location (bit area) of address uniquely assigned thereto. Accordingly, the sending multiplex node, which is the source of transmission of a data frame, sends an active signal at the first phase of each bit in the ACK signal area to the multiplex bus 10. The receiving multiplex nodes each detect the leading edge of the first phase of the bit area (in the ACK signal area of the transmitted data frame) assigned thereto, and at the same time sets the multiplex bus 10 active and maintains the active state up to the second phase, so that the bit concerned indicates logical

"1." On the other hand, when the data is not properly received, each of the receiving nodes does not set the multiplex bus 10 active, though it detected the leading edge of the first phase of the bit assigned thereto, and therefore, the second phase becomes passive, which indicates logical "0."

Also in this case, a PWM signal is used for the transmission code, like the transmission code in the ACK signal area. Namely, when a data frame is transmitted between the AS and AU multiplex nodes 21 and 22, for example, the AU multiplex node 22 sends an active signal at the first phase, and the AS multiplex node 21 transmits data at least in the second phase, in a manner timed with the first phase. Accordingly, the AS and AU multiplex nodes 21 and 22 can be easily synchronized.

As shown in FIG. 4, each multiplex node includes an interface circuit 50, a multiplex transmission control circuit 51, and a control circuit (CPU) 52.

The interface circuit 50 interfaces between the multiplex bus 10 and the multiplex transmission control circuit 51.

The multiplex transmission control circuit 51 includes an ACK table 53 for identifying individual nodes connectable to the network by means of bits. If the total number of the nodes is 32, for example, the ACK table 53 includes bit areas corresponding to the 32 nodes. The multiplex transmission control circuit 51 has the bit areas set corresponding to the individual groups mentioned above, and in an initialized state, the bits in these bit areas all represent logical "1," i.e., a state in which all of the 32 nodes are connected to the network. The multiplex transmission control circuit 51 of a receiving node determines whether or not the node thereof belongs to the specified group, on the basis of the data in the group specifying area of the frame supplied from the multiplex bus 10 through the interface circuit 50. If the node belongs to the specified group, and if the data is properly received, the multiplex transmission control circuit 51 sends an ACK signal acknowledging the data reception, and supplies an interrupt signal to the CPU 52. The multiplex transmission control circuit 51 of a sending node determines whether or not ACK signals have been returned from the multiplex nodes of the specified group, on the basis of the data in the group specifying area in the frame. If a required ACK signal is not returned, the multiplex transmission control circuit 51 retransmits the frame.

The CPU 52 controls various load devices connected thereto, and upon receiving the interrupt signal, determines that a required data frame has been received. The CPU 52 then reads out the data of the frame from the multiplex transmission control circuit 51, stores the data in an internal memory thereof, not shown, and uses the stored data as control parameters when controlling the load devices connected to the node.

Next, the communication between the multiplex nodes of the above multiplex transmission system will be described. In this embodiment, the first group specified by the least significant bit in the group specifying area of FIG. 2(a) as "0001" is the group of nodes related to the lighting, shown in FIG. 1, and the second group specified by the second bit in the group specifying area of FIG. 2(b) as "0010" is the group of nodes related to the doors, shown in FIG. 1. The third group specified in the group specifying area of FIG. 2(c) as "0011" includes the groups of nodes related to the lighting and doors, and the fourth group specified in the group specifying area of FIG. 2(d) as "1001" includes the groups of nodes related to the lighting and air conditioner, shown in FIG. 1.

In this embodiment, the transmission of a data frame is repeated three times at the maximum, and when a required ACK signal is not returned even though the frame transmission was carried out three times, the multiplex transmission control circuit updates the ACK table. When a data transmission is properly carried out, the multiplex transmission control circuit of each node returns an ACK signal to a location of address uniquely assigned thereto.

Referring to FIG. 5, a sending multiplex node (in this case, the CS multiplex node 20) transmits the data frame shown in FIG. 2(a) through the multiplex bus 10. Among the various multiplex nodes, the six multiplex nodes related to the lighting, i.e., the FR, FL, RR, RL, MET and CS multiplex nodes 11 to 14, 19 and 20, detect a request to send the ACK signal from the value "0001" in the group specifying area of the frame.

If the data is properly received, each of the multiplex nodes 11 to 14, 19 and 20 returns the ACK signal to the bit area of the address uniquely assigned thereto. In this embodiment, the multiplex nodes 11 to 14, 19 and 20 successively return the ACK signals to the bit areas assigned to the nodes 0 to 5 (see FIG. 2(a)) in the ACK signal area of the frame. Accordingly, the bit areas in the ACK signal area allotted to the nodes 0 to 5 are individually set to logical "1." Since, in this embodiment, the ACK signals from the six nodes are returned to an 8-node area in the ACK signal area, the ACK signal area becomes "11111100" as shown in FIG. 6. The multiplex nodes belonging to groups other than that related to the lighting are not specified, and therefore, do not return ACK signals.

The sending multiplex node compares the ACK signals returned to the ACK signal area with the information in the ACK table 53 thereof relating to the bit areas corresponding to the lighting group (information relating to the first to eighth bits from the left in the ACK table shown in FIG. 6).

If the returned ACK signals coincide with the information in the ACK table 53, the sending multiplex node determines that signals were properly transmitted, and thus ends the data transmission. In the illustrated example, since all bits in the ACK table 53 of

the sending multiplex node are set to logical "1" by the initialization, as shown in FIG. 6, the sending node determines that the returned signals do not coincide with the information in the table. In this case, the sending multiplex node repeats the transmission of the frame, three times at the maximum, until the ACK signals and the information in the ACK table 53 become coincident.

ACK signals are not returned from all of the eight nodes set in the ACK table 53 even if the same frame is transmitted three times, and therefore, the sending multiplex node regards the two nodes, which did not return the ACK signals, as not being connected to the multiplex bus. In accordance with this determination, the sending multiplex node determines that the information in the ACK table 53 relating to the bit areas corresponding to the lighting group must be updated. Thus, the sending multiplex node sets the bits in the bit areas corresponding to the two nodes which did not return the ACK signals to logical "0."

The multiplex nodes other than the sending multiplex node (the multiplex nodes of all groups) also individually carry out the above-mentioned ACK management in accordance with the returned ACK signals. Namely, the individual multiplex nodes determine that the information in the bit areas of the ACK tables 53 corresponding to the lighting group must be updated, and set the bits of the bit areas corresponding to the two nodes which did not return the ACK signals to logical "0" (see FIG. 6).

Suppose, then, the frame shown in FIG. 2(b) is transmitted from a sending multiplex node (in this case, the DFR multiplex node 15) through the multiplex bus 10. Among the multiplex nodes, those belonging to the group related to the doors, i.e., the DFR, DFL, DRR, DRL and CC multiplex nodes 15 to 18 and 23, detect a request to send the ACK signal from the value "0010" in the group specifying area of the frame.

When the data is properly received, each of the multiplex nodes 15 to 18 and 23 returns an ACK signal to the bit area in the ACK signal area at the address uniquely assigned thereto. In this embodiment, the multiplex nodes 15 to 18 and 23 successively return the ACK signals to the bit areas of the ACK signal area assigned to the nodes 8 to 11 and 15 (see FIG. 2(b)). Accordingly, the bit areas in the ACK signal area corresponding to the nodes 8 to 11 and 15 are set to logical "1." In this embodiment, since the ACK signals are returned from the five nodes to an 8-node area in the ACK signal area, the bits in the ACK signal area become "11110001" as shown in FIG. 6.

The sending multiplex node compares the ACK signals returned to the ACK signal area with the information in the ACK table 53 thereof relating to the bit areas corresponding to the door group (information relating to the ninth to sixteenth bits from the left in the ACK table shown in FIG. 6).

In the illustrated case, since the bits in these bit

areas of the ACK table 53 are set to logical "1" by the initialization, as shown in FIG. 6, the sending multiplex node determines that the returned signals do not coincide with the information in the table. In this case, the sending multiplex node repeats the transmission of the frame, three times at the maximum.

ACK signals are not returned from all of the eight nodes set in the ACK table 53 even if the same frame is transmitted three times, and therefore, the sending multiplex node regards the three nodes, which did not return the ACK signals, as not being connected to the multiplex bus. Then, the sending multiplex node determines that the information in the ACK table 53 relating to the bit areas corresponding to the door group must be updated, and thus sets the bits in the bit areas corresponding to the three nodes which did not return the ACK signals to logical "0."

The multiplex nodes other than the sending multiplex node (the multiplex nodes of all groups) also individually carry out the ACK management. Namely, the individual multiplex nodes set the bits of the bit areas corresponding to the three nodes which did not return the ACK signals to logical "0" (see FIG. 6).

Let it now be assumed that the frame shown in FIG. 2(d) is transmitted from a sending multiplex node through the multiplex bus 10. Among the multiplex nodes, those belonging to the groups related to the lighting and air conditioner, i.e., the FR, FL, RR, RL, MET and CS multiplex nodes 11 to 14, 19, 20 and the AS and AU multiplex nodes 21 and 22, detect a request to send the ACK signal from the value "1001" in the group specifying area of the frame

When the data is properly received, each of the multiplex nodes 11 to 14, 19, 20, 21 and 22 returns an ACK signal to the bit area in the ACK signal area at the address uniquely assigned thereto. In this embodiment, the multiplex nodes 11 to 14, 19, 20, 21 and 22 successively return the ACK signals to the bit areas of the ACK signal area assigned to the nodes 0 to 5, 24 and 25 (see FIG. 2(d)). Accordingly, the bit areas in the ACK signal area corresponding to the nodes 0 to 5, 24 and 25 are set to logical "1." In this embodiment, since the ACK signals are returned from the eight nodes (the six nodes of the lighting group and the two nodes of the air conditioner group) to a 16-node area in the ACK signal area, the bits in the ACK signal area become "1111110011000000" as shown in FIG. 6

The sending multiplex node compares the ACK signals returned to the ACK signal area with the information in the ACK table 53 thereof relating to the bit areas corresponding to the lighting and air conditioner groups (information in the first to eighth bits and the twenty-fifth to thirty-second bits from the left in the ACK table shown in FIG. 6).

In the illustrated example, since the first to sixth bits and the ninth and tenth bits from the left in the ACK table 53 are set to logical "1," as shown in FIG.

6, the sending multiplex node determines that the returned signals do not coincide with the information in the table. In this case, the sending multiplex node repeats the transmission of the frame, three times at the maximum.

ACK signals are not returned from all of the fourteen nodes set in the ACK table 53 even if the same frame is transmitted three times, and therefore, the sending multiplex node regards the six nodes, which did not return the ACK signals, as not being connected to the multiplex bus. Then, the sending multiplex node determines that the information in the ACK table 53 relating to the bit areas corresponding to the air conditioner group must be updated, and thus sets the bits in the bit areas corresponding to the six nodes which did not return the ACK signals to logical "0."

The multiplex nodes other than the sending multiplex node (the multiplex nodes of all groups) also individually carry out the ACK management. Namely, the individual multiplex nodes set the bits of the bit areas corresponding to the six nodes which did not return the ACK signals to logical "0" (see FIG. 6).

Suppose the data frame shown in FIG. 2(a) is transmitted from a sending multiplex node after two multiplex nodes connected to the multiplex bus 10 are newly added to the lighting group. A total of eight multiplex nodes belonging to the lighting group, i.e., the FR, FL, RR, RL, MET and CS multiplex nodes and the two multiplex nodes newly added, detect a request to send the ACK signal from the value "0001" in the group specifying area of the frame.

In this case, the multiplex nodes 11 to 14, 19 and 20 and the newly added two multiplex nodes successively return ACK signals to the bit areas (see FIG. 2(a)) in the ACK signal area assigned to the nodes 0 to 7. Accordingly, the bit areas in the ACK signal area corresponding to the nodes 0 to 7 are all set to logical "1." In this case, the ACK signals from the eight nodes are returned to the 8-node area in the ACK signal area, and therefore, the bits in the ACK signal area become "11111111" as shown in FIG. 6.

The sending multiplex node compares the ACK signals returned to the ACK signal area with the information in the ACK table 53 thereof relating to the bit areas corresponding to the lighting group.

In this case, the bits in the ACK signal area are all logical "1," and therefore, the sending multiplex node determines that two nodes were newly connected to the multiplex bus 10. Then, the sending multiplex node determines that the information in the ACK table 53 relating to the bit areas corresponding to the lighting group must be updated, and thus sets the bits in the bit areas corresponding to the two nodes which returned the ACK signals to logical "1."

The multiplex nodes other than the sending multiplex node (the multiplex nodes of all groups) also individually carry out the ACK management. Namely, the individual multiplex nodes set the bits of the bit

areas corresponding to the two nodes which returned the ACK signals to logical "1" (see FIG. 6).

FIG. 7(a)-(c) illustrate a message format for data frames according to a second embodiment of this invention. The data frames shown in FIG. 7 are characterized in that coding is used for specifying the groups. In this embodiment, the code "00" in the group specifying area specifies the first group, the code "01" specifies the second group, the code "10" specifies the third group, and the code "11" specifies the fourth group, for example.

Accordingly, also in the case of transmitting such data frames, ACK signals are returned from the nodes of the group specified in the group specifying area.

In the multiplex transmission system of this invention, nodes from which ACK signals are to be returned can be specified by groups, according to the contents of the data to be transmitted, and therefore, not all of the nodes are required to return the ACK signals each time a data frame is transmitted. Accordingly, the ratio of occupation of the ACK signal area to the entire frame can be reduced, and the ACK signals are efficiently returned from the required nodes only. Thus, in the multiplex transmission system of this invention, the number of nodes connectable to the network can be greatly increased, without lowering the transmission efficiency.

This invention is not limited to the above-described embodiments, and a mode may be additionally provided in which ACK signals are returned from all nodes. For example, one of the two bits in the group specifying area shown in FIG. 7 may be allotted to this mode to be used for the transmission of the ignition switch signal and the like. When this mode is set, ACK signals are returned from all of the nodes 11 to 23 connected to the multiplex bus 10.

In the second embodiment, the grouping is specified by a two-bit code. This invention, however, is not limited to such a configuration, and the individual groups may be assigned a plurality of bits, respectively. Namely, if each group is assigned 4 bits, for example, a total of sixteen combinations of node groups can be specified. Further, combinations of node numbers and group specifying codes for such combinations may be defined for each system and the definitions stored in each node, to carry out a communication.

Although the foregoing embodiments employ an acknowledge system in which a predetermined signal is sent to an ACK confirmation area when ACK signals are returned from all of the registered nodes, this invention is not limited to such a system. For example, the invention may be applied to a negative acknowledge system in which a predetermined signal is sent to the ACK confirmation area where all of the registered nodes did not return the ACK signals.

Further, although in the above embodiments, one node belongs to only one group, the nodes can indi-

vidually be set in different groups. For example, the AS multiplex node 21 shown in FIG. 1 may be set in such a manner that it belongs to the air conditioner group including the AU multiplex node 22, and also belongs to the group of the FR, FL, RR, RL, MET and CS multiples nodes 11 to 14, 19 and 20. In this case, a signal equally required by the nodes of the lighting group and the AS multiplex node 21, e.g., a signal from the tail-lamp (illumination) switch, can be transmitted by specifying the multiplex nodes of the lighting group only, and an ACK signal is returned from the AS multiplex node 21 requiring the tail-lamp (illumination) switch signal, without specifying both the multiplex nodes of the lighting group and those of the air conditioner group in the data transmission. Accordingly, in the multiplex transmission system of this invention, the ACK signal area can be reduced also in such a configuration, and the transmission efficiency can be further improved.

In the above embodiments, the AS multiplex node 21 alone is able to return the ACK signal to a plurality of areas, but a plurality of nodes may have such a function.

In the above embodiments, the ACK table provided in the multiplex transmission control circuit of each node has individual bits in the memory area thereof assigned to the respective nodes of a plurality of groups, but this invention is not limited to such a structure. For example, the invention may be applied to a system wherein the multiplex transmission control circuit includes an ACK table which stores data relating to only the nodes of certain groups for the ACK management. Further, the invention may be applied to a system wherein a single ACK table is shared by all nodes belonging to a plurality of groups and stores data relating to only the nodes of the group specified by the latest transmission frame for the ACK management. These systems are advantageous in the case wherein the relation between the individual groups is not significant. In this case, the multiplex transmission circuit in each node can be made small in scale, and the manufacturing cost can be reduced.

## Claims

1. A multiplex transmission system having a plurality of multiplex nodes(11-23) interconnected by a common multiplex transmission line(10) for transmitting a frame of data among the multiplex nodes through the multiplex transmission line(10), characterized in that when a data transmission is properly carried out, an acknowledge signal indicating a proper completion of the data transmission is returned to an acknowledge signal area provided in the frame of data corresponding to the respective multiplex nodes, the frame of data including an identifier which specifies one of groups

of predetermined combinations of multiplex nodes(11-14,19,20) from which the acknowledge signal is to be returned, and the multiplex nodes(11-14,19,20) which belong to a specified group returning the acknowledge signal in accordance with the identifier in the transmitted frame of data.

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2. A multiplex transmission system according to claim 1, characterized in that the identifier specifies on a bit-wise basis a group of multiplex nodes(11-14,19,20) from which the acknowledge signal is to be returned.

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3. A multiplex transmission system according to claim 1, characterized in that the identifier specifies by a binary code a group of multiplex nodes(11-14,19,20) from which the acknowledge signal is to be returned.

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4. A multiplex transmission system according to claim 1, 2, or 3 characterized in that each of the multiplex nodes includes memory means(53), which stores data indicating all multiplex nodes(11-23) from which the acknowledge signal is returned in a table corresponding to the respective groups of predetermined combinations of the multiplex nodes, specifies a group of multiplex nodes(11-14,19,20) from which the acknowledge signal is to be returned in accordance with the data stored in the table when a frame of data is transmitted, and returns the acknowledge signal in accordance with the data stored in the table when a frame of data is received.

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5. A multiplex transmission system according to any preceding claim, characterized in that the multiplex nodes(11-23) each belong to one group, or two or more groups.

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6. A multiplex transmission system according to any preceding claim, characterized in that the groups each comprise several or all of the multiplex nodes(11-23) interconnected by the multiplex transmission bus.

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FIG. 1

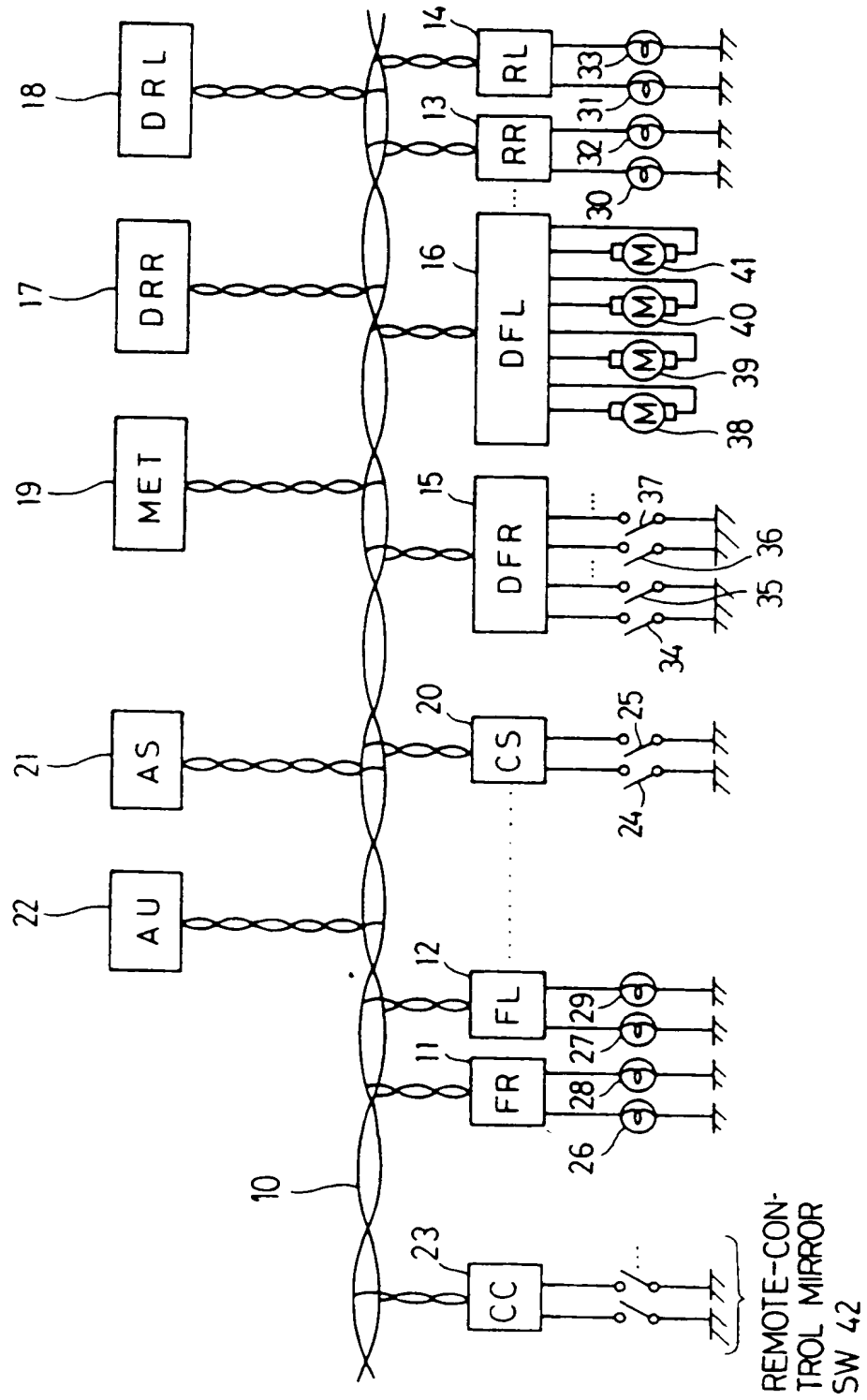


FIG. 2

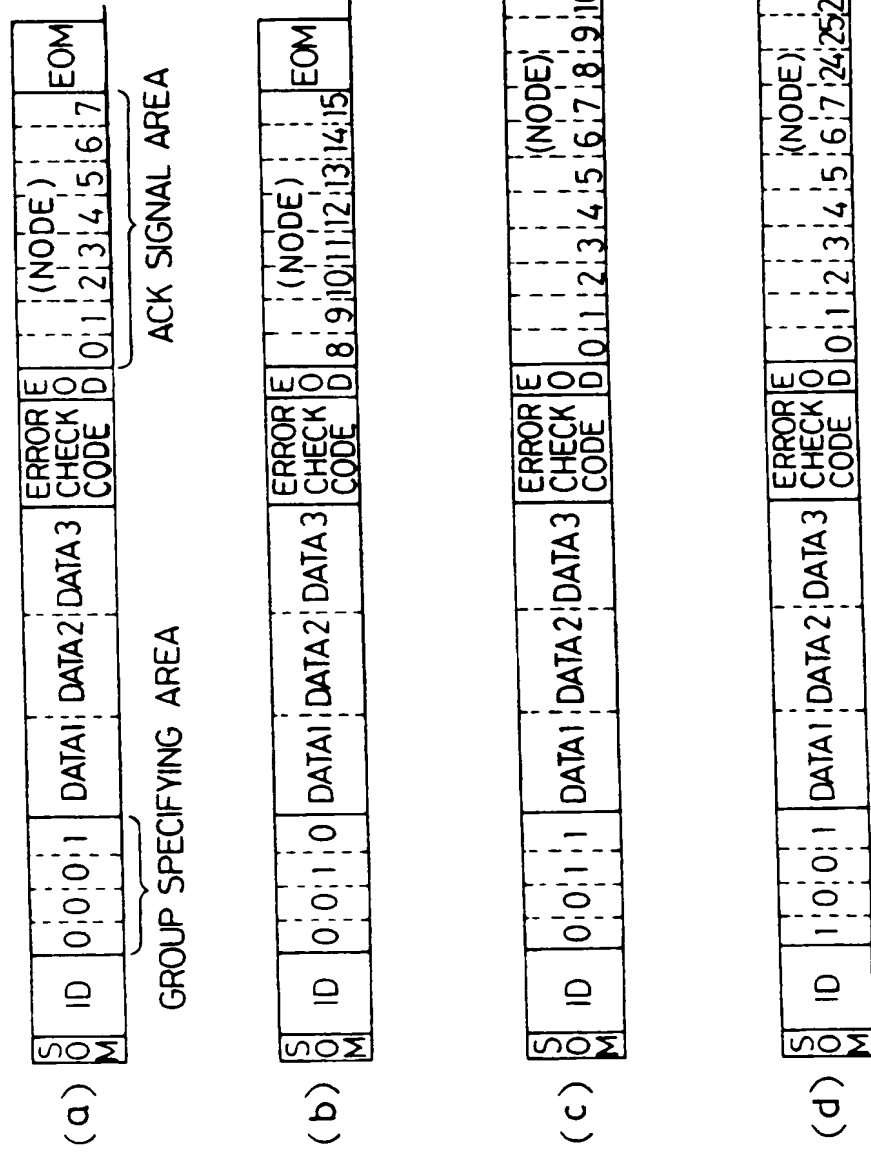


FIG. 3

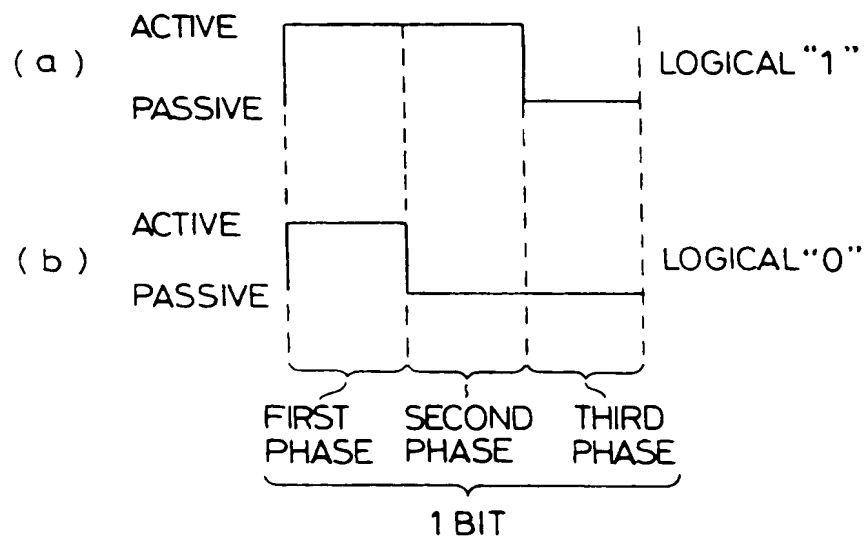


FIG. 4

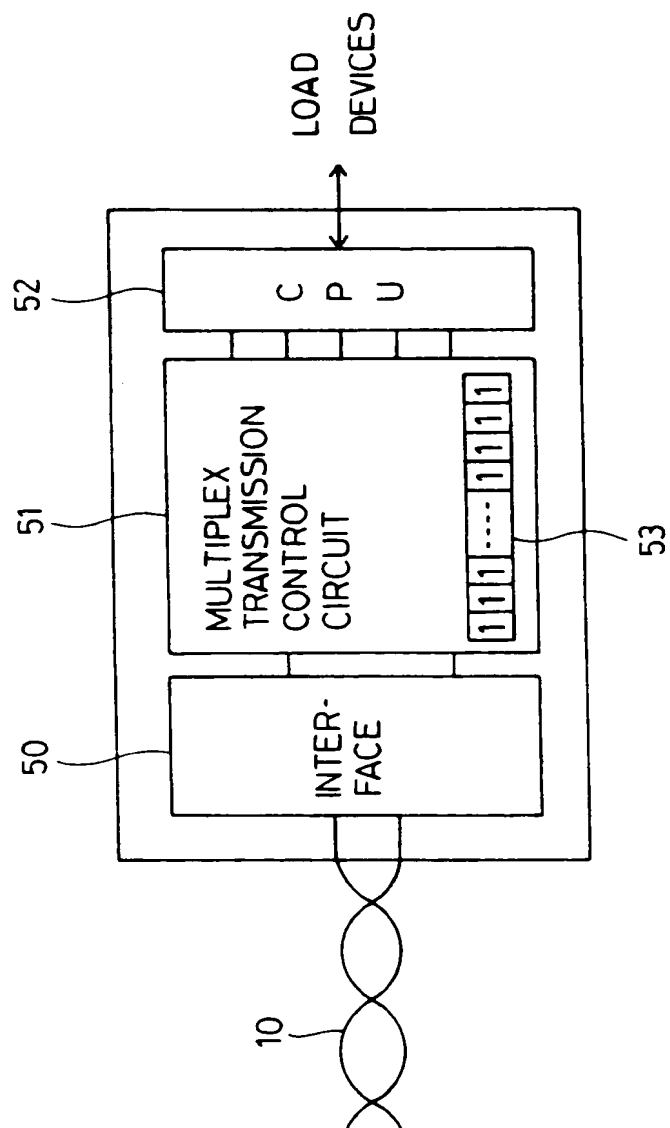


FIG. 5

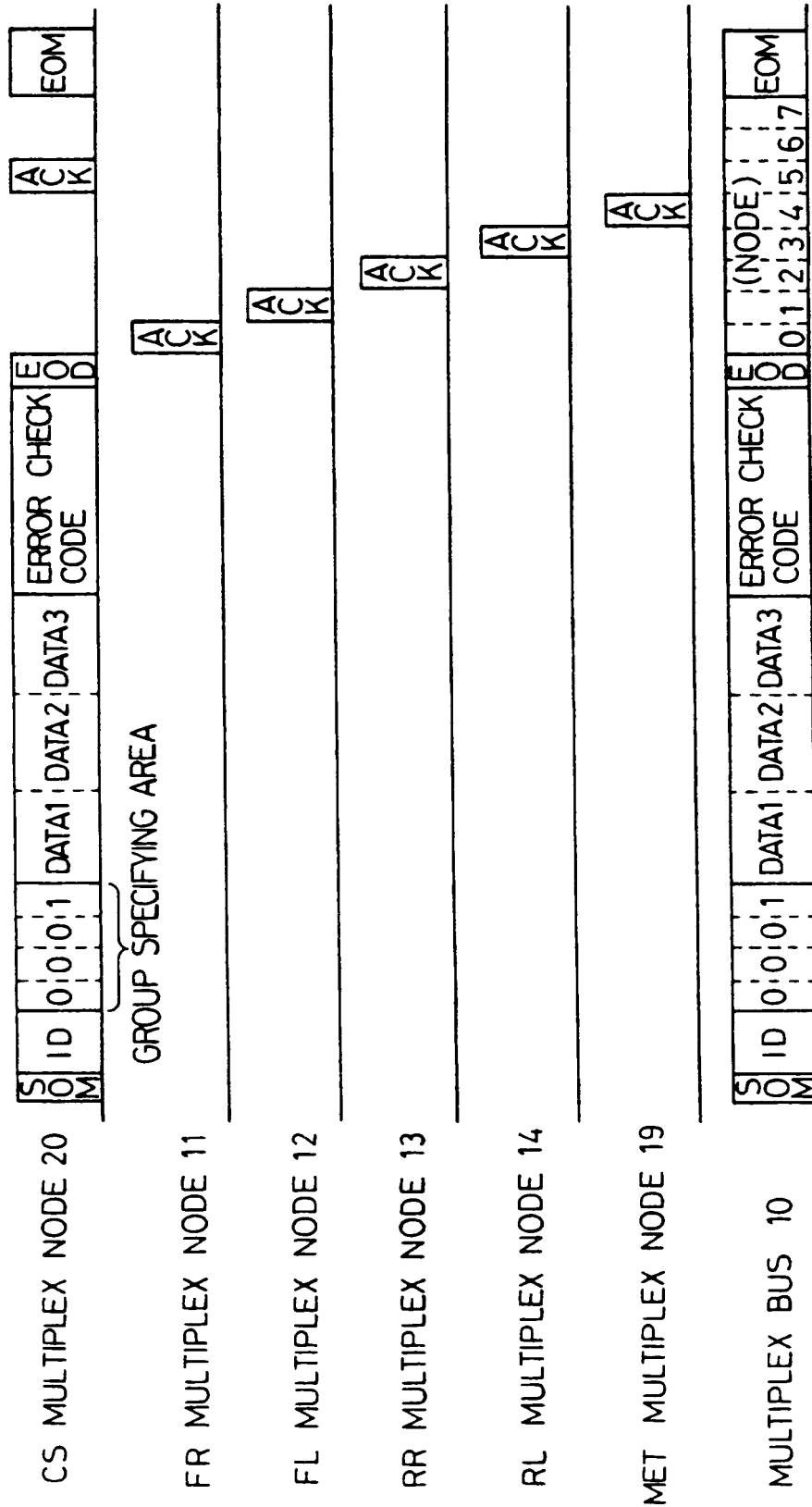
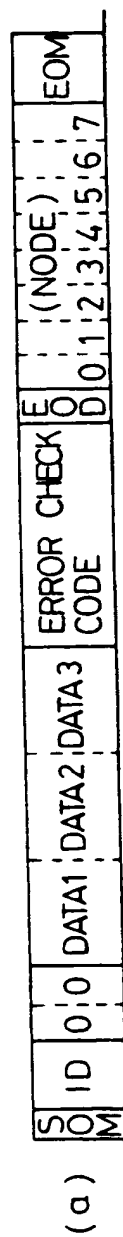
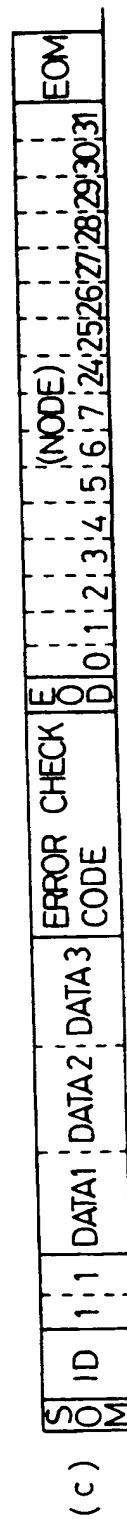
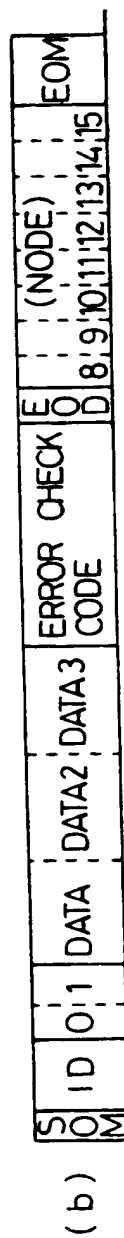




FIG. 7



GROUP SPECIFYING AREA





European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

EP 92 30 2902

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	EP-A-0 319 271 (FURUKAWA) * abstract * * page 11, line 55 - page 12, line 7 * ---	1-6	H04L12/40 H04L1/16 B60R16/02
Y	EP-A-0 217 571 (FORD) * page 13, line 9 - page 15, line 25 * ---	1-6	
A	MICROPROCESSING AND MICROPROGRAMMING, vol. 19, no. 2, February 1987, AMSTERDAM NL pages 153 - 166; D. DEL CORSO ET AL.: 'An Integrated Controller for Modified Inter-Integrated Circuit Protocol' * page 157, left column, line 13 - line 22 * -----	1-3, 5, 6	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H04L B60R
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 09 JULY 1992	Examiner MIKKELSEN C.
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons - - - - - &amp; : number of the same patent family, corresponding document</p>			

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